



Form PTO-1449 (modified 2/91)	U.S. DEPT. OF COMMERCE Patent and Trademark Office	Attorney Docket Number: 2102397-992750	Serial Number: 10/653,015
INFORMATION DISCLOSURE CITATION (Use several sheets if necessary)		Applicants: Bomy Chen et al.	
		Filing date: August 28, 2003	Group art unit: 2811

U.S. PATENT DOCUMENTS

Examiner Initial	Patent No. Publication No.	Date	Name	Class	Sub-class	Filing date if appropriate
<i>PHL</i>	10/358,601	02/03	Kianian			
	10/358,623	02/03	Hu et al.			
	10/757,830	01/04	Yeh et al.			
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FOREIGN PATENT DOCUMENTS

Document number	Date	Country	Class	Sub-class	Translation YES NO

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

<i>nm</i>	HAYASHI, Fumihiko and PLUMMER, James D., "A Self-Aligned Split-Gate Flash EEPROM Cell With 3-D Pillar Structure", 1999 Symposium on VLSI Technology Digest of Technical Papers, Center for Integrated System, Stanford University, Stanford, CA 94305, USA, pages 87-88
<i>nm</i>	SZE, Simon, "Physics of Semiconductor Devices", 2nd Edition, Wiley-Interscience, Basic Device Characteristics pages. 438-439
<i>nm</i>	BROWN, William D. et al.; "Nonvolatile Semiconductor Memory Technology, A Comprehensive Guide to Understanding And Using NVSM Devices", IEEE Press, pp. 33-34

Examiner: <i>Kang, Bonghee</i>	Date Considered: <i>09-30-04</i>
EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP '609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant.	

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	Applicant: Bomy Chen			
	Filing date: August 28, 2003		Group art unit: Not yet assigned	

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<i>DMH</i>	4,757,360	07/12/88	Farone			
	4,794,565	12/27/88	Wu et al.			
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	Document number	Date	Country	Class	Sub- class	Translation	
						YES	NO
<i>DMH</i>	EP0389721	10/03/90	Europe				

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

Examiner: <i>Kang, Donghee</i>	Date Considered: <i>09-30-04</i>
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